

<b>Notice of References Cited</b>	Application/Control No. 1	Applicant(s)/Patent Under Reexamination LEE, HIN-KWAI	
	Examiner Shambhavi Patel	Art Unit 2128	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,353,906	03-2002	Smith et al.	714/741
*	B	US-6,088,821	07-2000	Moriguchi et al.	714/724
*	C	US-6,877,123	04-2005	Johnston et al.	714/731
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	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Schmid et al. "Advanced Synchronous Scan Test Methodology for Multi Clock Domain ASICs" April 1999.
	V	Cummings, Clifford. "Syntheses and Scripting Techniques for Designing Multi-Asynchronous Clock Designs" Sunburst Design, 2001.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.